Flow2Vec: Value-Flow-Based Precise Code Embedding

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Contribution

A new code embedding approach which marries **precise static analysis** and recent advances in **high-order proximity embedding**, by preserving

- interprocedural alias-aware program dependence, and
- context-free language reachability,

to better support subsequent code analysis tasks, such as code summarization and semantic labeling.

Source Code

Model

Code Property Prediction





Yulei Sui @ OOPSLA, 17 Nov. 2020



semantic label	🖉 initialize	🖉 swap	Sort	
code snippet	<pre>inte(int[] mpArray, int size) { for (int i = 0; i < size; i++) mpArray[i] = i; return mpArray; }</pre>	<pre>< / > Source code #2</pre>		

Code semantic vector in geometric space



Code semantic vector in geometric space



Code semantic vector in geometric space



Code semantic vector in geometric space





Code semantic vector in geometric space



Structure-oblivious embedding

Source code → A bag of 'sentences'

```
int* ____(int[] myArray, int size)
{
   for (int i = 0; i < size; i++)
      myArray[i] = i;
   return myArray;
}</pre>
```

[1] Distributed representations of words and phrases and their compositionality. In NeurIPS '13

Structure-oblivious embedding



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Structure-oblivious embedding



[1] Distributed representations of words and phrases and their compositionality. In NeurIPS '13

[2] Distributed representations of sentences and documents. In ICML '14

}

Structure-oblivious embedding

Source code

A bag of 'sentences'

```
int* ____(int[] myArray, int size)
{
    for (int i = 0; i < size; i++)
        myArray[i] = i;
    return myArray;
}</pre>
```

int	*	(int	[]	myArray	,	int	size)
						_				
for	(int i	=	: 0						
•										
:										

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Structure-preserving embedding



[3] code2vec: Learning distributed representations of code. POPL .2019

Structure-preserving embedding



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Structure-preserving embedding



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Structure-preserving embedding



(a) Fail to capture asymmetric transitivity

(b) Alias-unaware

(a) Fail to capture asymmetric transitivity



program dependence graph

(b) Alias-unaware

(a) Fail to capture asymmetric transitivity





program dependence graph

(b) Alias-unaware

(a) Fail to capture asymmetric transitivity





 $V_A \cdot V_C^{^{\intercal}} > 0$ $C \rightarrow B \rightarrow C \checkmark$ Real reachability and correctly preserved $C \rightarrow B \rightarrow A \times$ Spurious reachability but imprecisely preserved

program dependence graph

(b) Alias-unaware

(a) Fail to capture asymmetric transitivity





 $A \rightarrow B \rightarrow C \checkmark$ Real reachability and correctly preserved C $\rightarrow B \rightarrow A \overleftrightarrow{}$ Spurious reachability but imprecisely preserved

program dependence graph

(b) Alias-unaware

A B Memory alias C



(a) Fail to capture asymmetric transitivity



Memory alias



2D-embedding space

 $V_A \cdot V_C^{^{\intercal}} > 0$ $C \to B \to C \checkmark$ Real reachability and correctly preserved $C \to B \to A \bigstar$ Spurious reachability but imprecisely preserved

program dependence graph

(b) Alias-unaware

 $V_{A} \cdot ~V_{C}^{^{\intercal}} <$ 0 $~A \rightarrow$ B \Rightarrow C \bigstar Real reachability but unsoundly preserved



(a) Fail to capture asymmetric transitivity





 $\begin{array}{cccc} A & \to B & \to C \checkmark \mbox{ Real reachability and correctly preserved} \\ V_A \cdot \ V_C^\intercal \geq 0 & & \\ & & C & \to B \ \to \ A \bigstar \ \mbox{ Spurious reachability but imprecisely preserved} \end{array}$

program dependence graph

(b) Alias-unaware

(c) Intraprocedural/context-insensitivity



2D-embedding space

The Aim of This Work



Flow2Vec: a high-order proximity code-embedding approach by preserving interprocedural alias-aware program dependence

Learning efforts (training, data, time)





The Aim of This Work



Flow2Vec: a high-order proximity code-embedding approach by preserving interprocedural alias-aware program dependence



















```
interprocedural value-flow
graph (IVFG)
```









h-th order reachability







h-th order reachability





























Phase (c) High-order proximity embedding



infeasible dependence relation between `stack` to `q`











Benchmarks



Total Line of Instructions:4,922,162 Total Methods:17,529 Total Pointers: 2,913,748 Total Objects: 190,157 Total Number of Calls:536,033 Total IVFGNodes: 4,637,301 Total IVFGEdges: 6,531,578

+Conducted machine: Intel Xeon Gold 6132 @ 2.60GHz CPUs and 128GB of RAM (All finish analyzing in 272.5mins)

Comparison with baselines

FLOW2VEC VS CODE2VEC FLOW2VEC VS CODE2SEQ)+16% +20.7% 34.80% 42.50% F1-score F1-score 55.50% 58.50%)+20.1%)+18.8% 34.20% 41.10% Recall Recall 54.30% 59.90%)+21.2% +13.2% 43.90% 35.50% Precision Precision 56.70% 57.10% CODE2VEC FLOW2VEC CODE2SEQ FLOW2VEC

F1-score under different lengths of code



FLOW2VEC VS CODE2VEC & CODE2SEQ

Ablation analysis



Thanks!

Q & A